

structure the language will encompass.

In response, claims 1 and 2 are amended to better define the inventive subject matter. In particular, claim 1 is amended to clarify that the mounting portion is for receiving a semiconductor device. Applicant asks the Examiner to note that the semiconductor device describes the function of the mounting portion. Additionally, claim 2 is amended to clarify the relationship between two punching holes and at least one depression.

The USPTO rejected claims 1-7 and 9-17 under 35 U.S.C. 102(b) as being anticipated by Honda (US 4,702,370). With all respect to the Examiner, the 35 U.S.C. 102(b) rejections of claims 1-7 and 9-17 are traversed.

Independent claims 1 and 5 relate to films. Contrary to the position of the USPTO, Honda '370 does not teach a film. Rather, Honda '370 teaches a carrier tape (not a film) that includes recesses that hold components. Additionally, in contrast to the independent claims, Honda '370 does not teach punching holes, or separation along depressions. Therefore, independent claims 1 and 5, and dependent claims 2-4 and 6-17, are allowable over Honda '370.

The USPTO also rejected claims 1-17 under 35 U.S.C. 103(a) as being obvious over Chenoweth (6,076,681). With all respect to the Examiner, the 35 U.S.C. 103(a) rejections of claims 1-17 are traversed.

Again, independent claims 1 and 5 relate to films. Contrary to the position of the USPTO, Chenoweth '681 does not teach a film. Rather, Chenoweth '681 teaches a carrier tape that includes recesses that hold components. Additionally, as best understood, in contrast to the claims, Chenoweth '681 does not teach punching holes or separation along depressions. Regarding Chenoweth '681, the Examiner asserts that elements 51-54 are punching holes.

However, those elements 51-54 are not described as having anything to do with punching or separation. Applicant requests the Examiner to reference Chenoweth '681, column 3, lines 58-60. Therefore, independent claims 1 and 5, and dependent claims 2-4 and 6-17, are allowable over Chenoweth '681.

The USPTO also rejected claims 1-17 under 35 U.S.C. 103(a) as being obvious over Honda '370. With all respect to the Examiner, the 35 U.S.C. 103(a) rejections of claims 1-17 are traversed. As provided above, Honda '370 does not teach or disclose a film. Therefore, since independent claims 1 and 5 relate to films, independent claims 1 and 5, and dependent claims 2-4 and 6-17 are allowable over Honda '370.


Applicants further amend claims 1-3 and 5 to better distinguish over the relied on references, to broaden the scope of the claims, and to prepare the claims for immediate allowance.

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to withdraw the outstanding rejection of the claims and to pass this application to issue.

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Respectfully submitted,

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**Version With Markings to Show Changes Made**

1. (Amended) A tape carrier package film for a liquid crystal display, comprising:

[~~an I-shaped~~] a tape carrier package part [~~including~~] having a mounting portion for a driving [~~device~~] integrated circuit, wherein the tape carrier package part is defined by a depression on the package film;

a peripheral part for securing the tape carrier package part, said peripheral part having a plurality of sprocket holes; and

a plurality of punching holes [~~provided at a side of the tape carrier package part, said punching holes~~] for reducing a connection between the tape carrier package part and the peripheral part.

2. (Amended) The tape carrier package as claimed in claim 1, wherein [~~center portions of~~] said punching holes [~~are formed consistent~~] are aligned with the depression.

5. (Amended) A tape carrier package film, comprising:

a peripheral part having sprocket holes; and

[~~an I-shaped~~] a tape carrier package part defined by a depression on the package film, said tape carrier package part having a mounting portion for receiving an integrated circuit;

wherein a plurality of punching holes are defined along said depression, the punching holes assisting separation of the tape carrier package part from the peripheral part.